

Edmonds' Apple I Specifications

Submitted by [Tom Owad](#) on May 22, 2004 - 10:17pm [Organization](#) [Apple](#) [Apple I](#)

All IC's are installed in sockets, thus simplifying repairs or hardware troubleshooting. The board has sockets for up to 8K bytes of the 16 pin, 4K type RAM, and the system is fully expandable to 65K via the edge connector. The system uses dynamic memory (4K bytes supplied), although static memory may also be used. All refreshing of dynamic memory, including all "off-board" expansion memory, is done automatically. The entire system timing, including the microprocessor clock and all video signals, originates in a single crystal oscillator. Furthermore, the printed circuit board contains a "breadboard area", in which the user can add additional "on-board" hardware (for example, extra PIA's, ACIA's, EROM's and so on). The board, as supplied, requires no more than 1.5 amps DC from the +5V supply, while the regulator is capable of supplying 3 amps. The remaining 1.5 amps DC from the +5V supply is available for user hardware expansion (provided suitable transformers are employed).

(Above data abstracted from Apple-1 Operation Manual.)

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SPECIFICATIONS

MICROPROCESSOR: MOS TECHNOLOGY 6502

Microprocessor Clock Frequency: 1.023 MHz

Effective Cycle Frequency: 0.960 MHz

VIDEO OUTPUT: Composite positive video, 75 ohms, level adjustable between 0 and +5V pp.

Line Rate: 15734 Hz

Frame Rate: 60.05 Hz

Format: 40 char/line, 24 lines, with automatic scrolling

Display Memory: Dynamic shift registers (1K x 7)

Character Matrix: 5 x 7

RAM MEMORY: 16-pin, 4K Dynamic, type 4096 (2104)

On-board RAM Capacity 8K bytes (4K supplied)

POWER SUPPLIES: +5 Volts @ 3 amps, +/-12 Volts @ 0.5 amps, and -5 Volts @ 0.5 amps

Input Power Requirements: 8 to 10 Volts AC (RMS) @ 3 amps, 26 to 28 Volts AC (RMS) Center-Tapped, 1A